EAST SEARCH

2/7/2007

L#	Hits	Search String	Databases
S54	375	S30 or S32 or S35 or S46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S38	40	S29 and ((instrument$3 or instrumentation) with (record$3 or identifY$3 or identified or identif	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S52	69	S29 and ((recursive or recursion) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S36	7	S29 and (monitor$3 with (simulation near2 event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S37	28	S29 and ((record$3 or identifY$3 or identified or identification) with (simulation near2 event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S39	10	S29 and ((monitor$3 or record$3 or identifY$3 or identified or identification) with (logical near	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S34	73	S29 and (monitor$3 near2 (element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S35	171	S29 and (hierarchical$3 with (element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S31	53	S29 and ((instrument$3 or instrumentation) with (simulat$3 near2 model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S30	109	S29 and (compil$3 with (simulat$3 near2 model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S32	198	S29 and (hierarchical$3 near2 design)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S29	5054	((integrated or digital) near2 circuit) with simulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S56	135	S53 and S55	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S57	302	S53 or S56	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S40	0	S29 and ((compiled near2 file) with listing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S42	44	S29 and ((instrument$3 or instrumentation) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S43	82	S29 and ((instrument$3 or instrumentation) with (event or element or component or object))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S44	0	S29 and (compil$3 with (bill near2 material))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S45	7	S29 and (compil$3 with (output$3 near2 file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S46	141	S29 and (("hardware description language" or HDL) near2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S51	71	S29 and (parent near2 (element or component or object or block or entity))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S41	63	S29 and (compiled near2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S49	7	S29 and (constraint with (data near2 structure))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S47	1183	S29 and (design with (element or component or object or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S50	52	S29 and (incremental$2 near2 (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S53	302	S31 or S34 or S36 or S37 or S38 or S39 or S41 or S42 or S43 or S45 or S49 or S50 or S51 c	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S55	273	S54 and S47	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S58	5054	((integrated or digital) near2 circuit) with simulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S59	109	S58 and (compil$3 with (simulat$3 near2 model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S60	53	S58 and ((instrument$3 or instrumentation) with (simulat$3 near2 model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S61	198	S58 and (hierarchical$3 near2 design)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S62	73	S58 and (monitor$3 near2 (element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S63	171	S58 and (hierarchical$3 with (element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S65	28	S58 and ((record$3 or identifY$3 or identified or identification) with (simulation near2 event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S64	7	S58 and (monitor$3 with (simulation near2 event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S74	7	S58 and (constraint with (data near2 structure))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S75	52	S58 and (incremental$2 near2 (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S66	40	S58 and ((instrument$3 or instrumentation) with (record$3 or identifY$3 or identified or identif	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S67	10	S58 and ((monitor$3 or record$3 or identifY$3 or identified or identification) with (logical near	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S68	63	S58 and (compiled near2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S69	44	S58 and ((instrument$3 or instrumentation) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

S70	82	S58 and ((instrument$3 or instrumentation) with (event or element or component or object))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S71	7	S58 and (compil$3 with (output$3 near2 file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S72	141	S58 and (("hardware description language" or HDL) near2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S73	1183	S58 and (design with (element or component or object or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S76	71	S58 and (parent near2 (element or component or object or block or entity))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S77	69	S58 and ((recursive or recursion) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S78	302	S60 or S62 or S64 or S65 or S66 or S67 or S68 or S69 or S70 or S71 or S74 or S75 or S76 c	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S79	375	S59 or S61 or S63 or S72	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S81	135	S78 and S80	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S80	273	S79 and S73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S82	302	S78 or S81	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S84	162	S83 and ((instrument$3 or instrumentation) with (simulat$3 or model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S83	5329	((integrated or digital) near2 circuit) with simulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S85	57	S84 and (hierarchical$3 with (design or element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S86	57	S84 and (monitor$3 near2 (logic or design or simulation or event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S87	72	S84 and ((instrument$3 or instrumentation) with (select$3 or specif$4 or defin$3 or identif$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S88	38	S84 and ((instrument$3 or instrumentation) with (constraint or specification or requirement))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S90	71	S84 and ((bill near2 material) or content)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S91	4	S84 and ((instrument$3 or instrumentation) with compatible)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S92	65	S84 and (("hardware description language" or HDL) with (design or file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S89	0	S84 and (file with ((bill near2 material) or content))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S93	45	S84 and ((parent or ancestor) near2 (element or component or block or entity))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S94	41	S84 and ((recursive or recursion) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S95	113	S85 or S86 or S87 or S88 or S90 or S91 or S92 or S93 or S94	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S109	1	S108 and (defin$3 with ("instrumentation entities"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S103	4	S97 and ((instrument$3 or instrumentation) with compatible)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S100	72	S97 and ((instrument$3 or instrumentation) with (select$3 or specif$4 or defin$3 or identif$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S98	57	S97 and (hierarchical$3 with (design or element or component or block))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S97	162	S96 and ((instrument$3 or instrumentation) with (simulat$3 or model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S96	5329	((integrated or digital) near2 circuit) with simulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S108	2	20050149313	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S104	65	S97 and (("hardware description language" or HDL) with (design or file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S107	113	S98 or S99 or S100 or S101 or S102 or S103 or S104 or S105 or S106	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S106	41	S97 and ((recursive or recursion) with (compil$3 or compilation))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S105	45	S97 and ((parent or ancestor) near2 (element or component or block or entity))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S102	71	S97 and ((bill near2 material) or content)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S99	57	S97 and (monitor$3 near2 (logic or design or simulation or event))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S101	38	S97 and ((instrument$3 or instrumentation) with (constraint or specification or requirement))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

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Results of search set S91:

Document Kind Codes	Title	Issue Date	Current OR	Abstract
US 20060148429 A1	Transmission path simulation method and transmission path simulator	20060706	455/115.1	

US 20060122818 A1	Method, system and program product for defining and recording threshold-qualified count eve	20060608	703/17
US 20060094365 A1	Channel simulator and wireless apparatus evaluation method	20060504	455/67.11
US 20060089827 A1	Method, system and program product for defining and recording minium and maximum event	20060427	703/17
US 20060089826 A1	Method, system and program product for defining and recording minimum and maximum cou	20060427	703/17
US 20050283350 A1	Flexible SPDIF verification tool	20051222	703/19
US 20050149313 A1	Method and system for selective compilation of instrumentation entities into a simulation mod	20050707	703/22
US 20050149309 A1	Method, system and program product supporting user tracing in a simulator	20050707	703/14
US 20050049842 A1	Method, system and program product that automatically generate coverage instrumentation f	20050303	703/14
US 20040214150 A1	Interaction education system for teaching patient care	20041028	434/273
US 20040152056 A1	Method and apparatus for simulating a clinical trial	20040805	434/219
US 20040126746 A1	Medical physiological simulator including a conductive elastomer layer	20040701	434/262
US 20030237078 A1	Incorporating simulation analysis instrumentation into HDL models	20031225	717/155
US 20030191869 A1	C-API instrumentation for HDL models	20031009	719/328
US 20030191621 A1	Method and system for reducing storage and transmission requirements for simulation results	20031009	703/17
US 20030191620 A1	Dynamic loading of C-API HDL model instrumentation	20031009	703/17
US 20030191618 A1	Method and system for reducing storage requirements of simulation data via keyword restricti	20031009	703/13
US 20030191617 A1	Method and system for selectively storing and retrieving simulation data utilizing keywords	20031009	703/13
US 20030135354 A1	Tracking converage results in a batch simulation farm network	20030717	703/13
US 20030125915 A1	Count data access in a distributed simulation environment	20030703	703/13
US 20030101382 A1	Fail thresholding in a batch simulation farm network	20030529	714/39
US 20030101041 A1	Annealing harvest event testcase collection within a batch simulation farm	20030529	703/22
US 20030101039 A1	Maintaining data integrity within a distributed simulation environment	20030529	703/16
US 20030101038 A1	Centralized disablement of instrumentation events within a batch simulation farm network	20030529	703/16
US 20030101035 A1	Non-redundant collection of harvest events within a batch simulation farm network	20030529	703/13
US 20030073060 A1	Interactive education system for teaching patient care	20030417	434/262
US 20020161564 A1	Method for modeling a reflected electrical wave in a digital simulation	20021031	703/13
US 20020128809 A1	Randomized simulation model instrumentation	20020912	703/17
US 20020123875 A1	Hierarchical processing of simulation model events	20020905	703/17
US 20020123874 A1	Detecting events within simulation models	20020905	703/17
US 20020123873 A1	Signal override for simulation models	20020905	703/17
US 20020120922 A1	Embedded hardware description language instrumentation	20020829	717/143
US 20010011212 A1	METHOD AND APPARATUS FOR GATE-LEVEL SIMULATION OF SYNTHESIZED REGIST	20010802	703/22
US 7162401 B1	Monitoring of resources that are being modeled by simulation or emulation	20070109	703/13
US 7158924 B2	Dynamic loading of C-API HDL model instrumentation	20070102	703/17
US 7143019 B2	Maintaining data integrity within a distributed simulation environment	20061128	703/13
US 7143018 B2	Non-redundant collection of harvest events within a batch simulation farm network	20061128	703/13
US 7117458 B1	Identifying specific netlist gates for use in code coverage testing	20061003	716/4
US 7114954 B2	Interaction education system for teaching patient care	20061003	434/262
US 7092868 B2	Annealing harvest event testcase collection within a batch simulation farm	20060815	703/22
US 7092864 B2	Signal override for simulation models	20060815	703/14
US 7085703 B2	Count data access in a distributed simulation environment	20060801	703/17
US 7043071 B2	Soft defect printability simulation and analysis for masks	20060509	382/144
US 7039574 B1	Naming and managing simulation model events	20060502	703/17
US 7027971 B2	Centralized disablement of instrumentation events within a batch simulation farm network	20060411	703/14
US 6978231 B2	Embedded hardware description language instrumentation	20051220	703/14
US 6976240 B2	Simulation using design geometry information	20051213	716/19
US 6954911 B2	Method and system for simulating resist and etch edges	20051011	716/4
US 6941257 B2	Hierarchical processing of simulation model events	20050906	703/15

US 6934885 B2	Fail thresholding in a batch simulation farm network	20050823	714/33
US 6920418 B2	Detecting events within simulation models	20050719	703/17
US 6892171 B2	Method for modeling a reflected electrical wave in a digital simulation	20050510	703/13
US 6769122 B1	Multithreaded layered-code processor	20040727	718/102
US 6758676 B2	Interactive education system for teaching patient care	20040706	434/262
US 6751583 B1	Hardware and software co-simulation including simulating a target processor using binary trai	20040615	703/17
US 6745372 B2	Method and apparatus for facilitating process-compliant layout optimization	20040601	716/2
US 6701491 B1	Input/output probing apparatus and input/output probing method using the same, and mixed e	20040302	716/4
US 6503087 B1	Interactive education system for teaching patient care	20030107	434/262
US 6470482 B1	METHOD AND SYSTEM FOR CREATING, DERIVING AND VALIDATING STRUCTURAL DE	20021022	716/6
US 6470478 B1	Method and system for counting events within a simulation model	20021022	716/4
US 6350943 B1	Electric instrument amplifier	20020226	84/603
US 6336087 B1	Method and apparatus for gate-level simulation of synthesized register transfer level design v	20020101	703/15
US 6240376 B1	Method and apparatus for gate-level simulation of synthesized register transfer level designs	20010529	703/15
US 6223142 B1	Method and system for incrementally compiling instrumentation into a simulation model	20010424	703/15
US 6212491 B1	Automatic adjustment for counting instrumentation	20010403	703/14
US 6208954 B1	Method for scheduling event sequences	20010327	703/16
US 6202042 B1	Hardware simulator instrumentation	20010313	703/16
US 6195629 B1	Method and system for selectively disabling simulation model instrumentation	20010227	703/17
US 6195627 B1	Method and system for instrumenting simulation models	20010227	703/14
US 5941710 A	Apparatus and method of simulating the determination of continuous blood gases in a patient	19990824	434/272
US 5910903 A	Method and apparatus for verifying, analyzing and optimizing a distributed simulation	19990608	703/6
US 5890908 A	Apparatus for and method of simulating the injection and volatilizing of a volatile drug	19990406	434/268
US 5882207 A	Apparatus and method for quantifying fluid delivered to a patient simulator	19990316	434/268
US 5868579 A	Apparatus and method for simulating lung sounds in a patient simulator	19990209	434/266
US 5867399 A	System and method for creating and validating structural description of electronic system fron	19990202	716/18
US 5801958 A	Method and system for creating and validating low level description of electronic design from	19980901	716/18
US 5779484 A	Apparatus and method of stimulating breathing sounds	19980714	434/266
US 5772443 A	Apparatus and method for detecting and identifying a drug	19980630	434/272
US 5772442 A	Apparatus and method for simulating bronchial resistance or dilation	19980630	434/265
US 5769641 A	Apparatus and method for synchronizing cardiac rhythm related events	19980623	434/272
US 5633812 A	Fault simulation of testing for board circuit failures	19970527	703/15
US 5623418 A	System and method for creating and validating structural description of electronic system	19970422	716/1
US 5604895 A	Method and apparatus for inserting computer code into a high level language (HLL) software	19970218	703/13
US 5584701 A	Self regulating lung for simulated medical procedures	19961217	434/272
US 5555201 A	Method and system for creating and validating low level description of electronic design from	19960910	716/1
US H001590 H	Portable aircraft instrumentation data simulator	19960903	703/16
US 5544067 A	Method and system for creating, deriving and validating structural description of electronic sy:	19960806	703/14
US 5490783 A	Flight simulator having active electronic display controls	19960213	434/35
US 5316002 A	Nasopharyngealometric apparatus and method	19940531	600/463
US 5065147 A	Method and apparatus for simulating analog display in digital display test instrument	19911112	345/440.1
US 5062341 A	Portable drum sound simulator generating multiple sounds	19911105	84/702
US 4959015 A	System and simulator for in-flight threat and countermeasures training	19900925	434/2
US 4878179 A	Interactive diagnostic methodology and apparatus for microelectronic devices	19891031	716/4
US 4719834 A	Enhanced characteristics musical instrument	19880119	84/652
US 4682526 A	Accompaniment note selection method	19870728	84/613
US 4630517 A	Sharing sound-producing channels in an accompaniment-type musical instrument	19861223	84/613
US 4492823 A	Dual timing circuit for telephone subscriber's instrument	19850108	379/387.01

US 4476765 A	Electronic music signal generator	19841016	84/604
US 4426904 A	Envelope control for electronic musical instrument	19840124	84/627
US 4400789 A	Magnetic heading simulator	19830823	703/13
US 4368432 A	Sine wave generator for different frequencies	19830111	327/107
US 4108040 A	Electronic musical instrument	19780822	84/608
US 4067253 A	Electronic tone-generating system	19780110	84/687
US 3902393 A	Automatic rhythm control circuit for musical instrument accompaniment	19750902	84/668
US 3585891 A	AN ELECTRONIC RHYTHM GENERATOR PARTICULARLY SUITABLE FOR INTEGRATED	19710622	84/667
US 20030101382 A	Fail event tracking method for use in digital circuit simulation, involves receiving fail event pac	20030529	
US 6470478 B	Event counting method for simulation of digital circuit design, involves generating linear feedt	20021022	
US 20020123875 A	Computer readable recorded medium storing digital circuit designing and simulating program	20020905	
US 6223142 B	Method for compiling instrumentation logic into simulation model of digital circuit design, invol	20010424	
US 6212491 B	Counting rate adjusting method involves including design entity sequenced in accordance wit	20010403	
US 6202042 B	Logical failure detection for hardware accelerated simulation model of digital circuit, by model	20010313	
US 6195629 B	Instrumentation entity output disabling method involves masking output signal selectively by c	20010227	
US 6195627 B	Computer aided design and verification for simulating digital circuit design model, involves uti	20010227	

Interference checked

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L#	Hits	Search String	Databases
L1	1352	((integrated or digital) near2 circuit) with simulat$3	US-PGPUB
L2	47	1 and ((instrument$3 or instrumentation) with (simulat$3 or model))	US-PGPUB
L3	24	2 and (hierarchical$3 with (design or element or component or block))	US-PGPUB
L4	24	2 and (monitor$3 near2 (logic or design or simulation or event))	US-PGPUB
L5	30	2 and ((instrument$3 or instrumentation) with (select$3 or specif$4 or defin$3 or identif$4))	US-PGPUB
L6	23	2 and ((instrument$3 or instrumentation) with (constraint or specification or requirement))	US-PGPUB
L7	1	2 and ((instrument$3 or instrumentation) with compatible)	US-PGPUB
L8	31	3 or 4 or 5 or 6 or 7	US-PGPUB
L9	1	8 and (constraint.CLM.)	US-PGPUB
L10	1	8 and (compatible.CLM.)	US-PGPUB
L11	1	8 and ("entities defined".CLM.)	US-PGPUB
L12	1	9 or 10 or 11	US-PGPUB

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Results of search set S91:

Document Kind Codes	Title	Issue Date	Current OR	Abstract
US 20060148429 A1	Transmission path simulation method and transmission path simulator	20060706	455/115.1	
US 20060122818 A1	Method, system and program product for defining and recording threshold-qualified count eve	20060608	703/17	
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